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| <u>L14</u> | l10 and L13 | 0 | <u>L14</u> |
| <u>L13</u> | l9 with l12 | 209 | <u>L13</u> |
| <u>L12</u> | l7 near2 (location or address) | 34640 | <u>L12</u> |
| <u>L11</u> | l9 near4 l1 | 5683 | <u>L11</u> |
| <u>L10</u> | l4 and l5 and l9 | 1400 | <u>L10</u> |
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| <u>L8</u> | simultaneous\$ or concurrent\$ or parallel\$ or ("same" adj2 time) | 5192077 | <u>L8</u> |
| <u>L7</u> | L6 or consecutive\$ | 1329568 | <u>L7</u> |
| <u>L6</u> | sequential\$ or contiguous\$ | 1145462 | <u>L6</u> |
| <u>L5</u> | l2 near2 l1 | 8929 | <u>L5</u> |
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» Key

IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **Comparison of buffering strategies for asymmetric packet switch modules**
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[Selected Areas in Communications, IEEE Journal on](#)
 Volume 9, Issue 3, April 1991 Page(s):428 - 438
 Digital Object Identifier 10.1109/49.76642
[AbstractPlus](#) | Full Text: [PDF\(804 KB\)](#) IEEE JNL
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- ☐ 2. **A decision feedback equalizer with time-reversal structure**
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- ☐ 3. **Analysis of packet loss processes in high-speed networks**
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- ☐ **9. A performance study of virtual output queued switches with heterogeneous bursty traffic**
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| #3 | ((buffer)<in>metadata) | 15308 |
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